Q.P. Code: 19EC0421 Reg. No: SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS) BTech II Year I Semester Regular Examinations February-2021 MICROPROCESSORS & MICROCONTROLLERS (Common to CSE & CSIT) Time: 3 hours Max. Marks: 60 (Answer all Five Units  $5 \times 12 = 60$  Marks) UNIT-I With a neat sketch explain the operation of Microprocessor Controlled Temperature 1 12M System (MCTS). OR a Differentiate between μP & μC. 2 **7M b** Explain the terms i) SSI ii) MSI iii) LSI iv) VLSI v) ULSI **5M** UNIT-II a List out the important features (any 12) of 8085 microprocessor. 3 **6M b** Sketch neat block diagram of 8085 microprocessor. **6M** a Draw the pin diagram of  $8085 \mu P$ . **7M b** Define the following pins: **5M** i) READY ii) ALE iii) RESET OUT iv) HOLD & HLDA. UNIT-III With the help of a neat block diagram, Explain the internal architecture of 8051 5 12M microcontroller in detail. OR a Compare serial communication and parallel communication. 3M **b** Explain how the 8051 μC transfers the data using serial port. 9M **UNIT-IV** a Define addressing mode. 2M**b** List various addressing modes of 8051 microcontroller and explain them with an 10M example each. OR a Write an assembly program of 8051 µC to divide two 8-bit numbers and store the **6M** result in a memory location. **b** Write an assembly program of 8051 μC to subtract two 8-bit numbers and store the **6M** result in a memory location. **UNIT-V** a List the features of 16X2LCDdisplay. 3M**b** Draw and explain the pin Diagram of 16x2LCD display. **9M** OR 10 a Write a short note on 7-Segement display. 3M

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9M

b With the help of a neat diagram, show the interfacing of 7- segment display with

8051 µC and explain its operation.